

CALL FOR PAPERS

The 6th IEEE Symposium on Embedded Multicore SoCs

MCSoc-12

The University of Aizu, Aizu-Wakamatsu, Japan
September 20-22, 2012



Sponsored by IEEE and IEEE Technical Committees on Microprocessors and Microcomputers and the Univ. of Aizu. In cooperation with IEEE sendai section.

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Call for papers

The International Symposium on Embedded Multicore Systems-on-chip (MCSoc-12) aims at providing the world's premier forum of leading researchers in the embedded multicore SoCs software, tools and applications design areas for Academia and industries. Prospective authors are invited to submit paper of their works. Submission of a paper implies that at least one of the authors will have a full registration to the symposium upon acceptance of the paper.

Topics of Interest

- Software for Embedded Multicore:** Compilers, cross assemblers, programming, memory management, object-oriented aspects, virtual machines, concurrent software for SoCs,...
- Hardware for Embedded Multicore:** SoCs, DSPs, hardware specification, modeling, synthesis, low power simulation and analysis, reliable, performance modeling, security issues.
- Embedded Multicore System Architecture:** Multicore, Many-core, reconfigurable platforms, memory management support, communication, protocols, real-time systems.
- Network-on-chip:** NoC architecture, Power and energy issues in NoC, Application specific NoC design, Timing, synchronous /asynchronous communication, RTOS support for NoC, Modeling, simulation, NoC support for MCSoc, NoCs for FPGAs and structured ASICs, NoC design tools.
- Multicore Real-time Systems:** Software, distributed real-time systems, real-time kernels, task scheduling, multitasking design.
- Multicore Testing:** Design-for-test, test synthesis, built-in self-test, embedded test for MCSoc,...
- Multicore Practices & Benchmarks:** Software, tools, trends, emerging technologies, experience maintaining benchmark suites, representation, tools, copyrights, maintenance, ...
- Packaging Technologies for MCSocs**
- Compilers, and RTOS**
- Embedded Systems and Network Protocols**
- Cloud Computing friendly Embedded Systems and Multicore Systems**

Submission Guidelines

The papers should be prepared according to the IEEE CS format, and no longer than 8 pages. Each submitted paper will be reviewed by at least three reviewers and will be evaluated based on originality, significance to Symposium's topics, organization, and technical quality. Submission of a paper implies that at least one of the authors will have a full registration to the symposium upon acceptance of the paper. Proceedings of the symposium will be published by IEEE Computer Society Press. All submitted papers to the MCSoc-12 should be submitted online.

Important Dates

Full papers submission : **March 16, 2012**
Acceptance notification : May 11, 2012
Camera ready paper : June 15, 2012
Symposium : September 20-22, 2012

URL: <http://web-ext.u-aizu.ac.jp/~benab/conferences/mcsoc-12/>

Venue : The University of Aizu, Tsuruga, Ikki-machi, Aizu-Wakamatsu City, Fukushima, 965-0826, Japan.